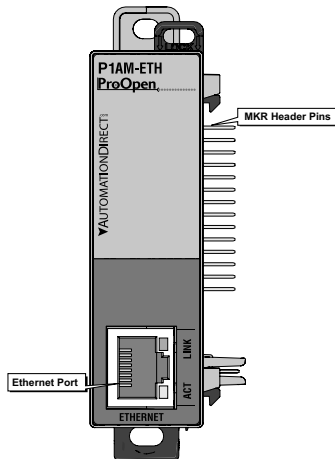# AUTOMATIONDIRECT.com
Productivity Open

P1AM-ETH Arduino® MKR Compatible Shield

The P1AM-ETH is a housed Arduino MKR form factor Ethernet Shield based on the Wiznet W5500 Ethernet Controller. It connects to the left side of the P1AM-100 CPU and most Arduino MKR form factor boards.

P1AM-ETH
ProOpen

AUTOMATIONDIRECT!

MKR Header Pins

Ethernet Port

LINK

ACT

ETHERNET

General Specifications	
Operating Temperature	0° to 60°C (32° to 140°F)
Storage Temperature	-20° to 70°C (-4° to 158°F)
Humidity	5 to 95% (non-condensing)
Environmental Air	No corrosive gases permitted
Vibration	IEC60068-2-6 (Test Fc)
Shock	IEC60068-2-27 (Test Ea)
Heat Dissipation	750mW
Enclosure Type	Open Equipment
Power Budget	150mA / 5V
Recommended Library	Arduino Ethernet (See back for details)
Module Location	Connects to the left side of the P1AM-100 CPU. P1-01AC/02AC can connect to the left side of the CPU.
Weight	20g (0.8 oz)
Agency Approvals	UL 61010-1 and UL 61010-2-201 File E139594, Canada & USA CE*

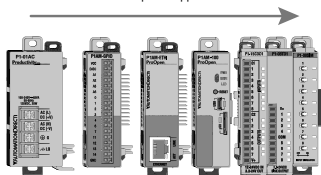*See CE Declaration of Conformance for details.

Link to GitHub

Link to additional resources

Module Installation

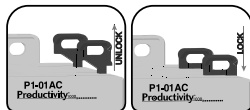**WARNING:** Do not add or remove modules with field power applied.

Step One: With latch in "locked" position, align connectors on the side of each module and stack by pressing together. Click indicates lock is engaged.

Check all latches are secure after modules are connected.

Step Two: Attach field wiring using the removable terminal block or *ZIP*Link wiring system.

WIRE STRIP LENGTH

MAX
MIN

Step Three: To unstack modules, pull locking latch up into the unlocked position and then pull modules apart.

NOTE: This module is based on open-source programming, which means that all operating support must be provided from 3rd party libraries and/or custom programmed applications. AutomationDirect.com does not offer drivers, stacks, or libraries for this product, but they are readily available online free of charge. AutomationDirect.com makes no guarantee of the performance of this module when using 3rd party libraries other than the hardware level as defined and implied herein.

Document Name	Edition/Revision	Date
P1AM-ETH-DS	1st Edition	10/30/2019