

Analog Voltage Output Modules

F2-08DA-2 8-Channel Voltage Analog Output <---->	
Number of Channels	8, single-ended, 1 common
Output Ranges	0 to 5V, 0 to 10V
Resolution	12 bit (1 in 4096)
Output Type	Voltage sourcing
Digital Output Points Required	16 (Y) output points (12 binary data bits, 3 channel ID bits, 1 output enable bit)
Base Power Required 5VDC	60 mA
External Power Supply	21.6-26.4 VDC, 140 mA (outputs fully loaded)
Peak Output Voltage	15 VDC (clamped by transient voltage suppressor)
Load Impedance	1-10 kΩ
Load Capacitance	0.01 μF maximum
PLC Update Rate	1 channel per scan maximum (D2-230 CPU) 8 channels per scan maximum (D2-240, D2-250(-1) and D2-260 CPUs)
Conversion Settling Time	400 μs maximum (full scale change) 4.5 ms to 9ms for digital out to analog out

Linearity Error (end to end)	±1 count (±0.025% of full scale) maximum
Full Scale Calibration Error	±12 counts max. unipolar @ 25°C (77°F)
Offset Calibration Error	±3 counts max., unipolar @ 25°C (77°F)
Accuracy vs. Temperature	±57 ppm/°C full scale calibration change (including maximum offset change of 2 counts)
Maximum Inaccuracy	±0.3% @ 25°C (77°F) ±0.45% @ 0-60°C (32-140°F)
Operating Temperature	0° to 60°C (32° to 140°F)
Storage Temperature	-20° to 70°C (-4° to 158°F)
Relative Humidity	5 to 95% (non-condensing)
Environmental air	No corrosive gases permitted
Vibration	MIL STD 810C 514.2
Shock	MIL STD 810C 516.2
Noise Immunity	NEMA ICS3-304
Terminal Type (included)	Removable; D2-810CON

One count in the specification table is equal to one least significant bit of the analog data value (1 in 4096).

NOTE 1: Shields should be connected to the 0V of the module.

