DL205 Analog I/O Modules



Please include the Manual Number and Issue, both shown below, when communicating with Technical Support regarding this publication.

Manual Number: D2-ANLG-M

Issue: 7th Edition Rev. G

Issue Date: 6/20

Publication History		
Issue	Date	Description of Changes
1st Edition	1/94	Original Edition
2nd Edition	4/95	New Edition
3rd Edition	9/97	Added new modules
4th Edition	4/99	Added new modules
5th Edition	5/00	Added new modules
6th Edition	4/02	Added new modules
Rev. A	6/02	Added D2-250-1 and D2-260 CPUs and removed references to D2-250 CPU. (Note: D2-250 has same functionality as a D2-250-1 except for local expansion capability).
Rev. B	8/02	Minor corrections
7th Edition	8/05	Added new F2-8AD4DA chapters 15 and 16 and miscellaneous minor changes.
Rev A	11/06	Added information about changes to F2-04THM jumper link locations in chapter 7.
Rev. B	4/10	Added information about jumper link locations and some input specification changes on F2-04AD-1, F2-04AD-2, F2-08AD-1, F2-08AD-2, and F2-02DAS-2 modules. Also added R Wide input range to F2-04THM specification table.
Rev. C	9/17	Minor changes - changed to download version only
Rev. D	12/17	Made correction to wiring diagram in Chapter 11
Rev. E	12/18	Added D2-262 CPU and revised complete manual.
Rev. F	5/19	Chapter 16 - revised ladder examples 1, 2, and 3; updated V-memory location tables.
Rev. G	6/20	Chapter 8 & 9 - revised external power supply value requirements in data tables.